

<b>In re application of:</b>	Enrique Musoll Adolfo M. Nemirovsky Mario D. Nemirovsky Narendra Sankar
<b>Serial No.:</b>	09/706154
<b>Filed:</b>	11/03/00
<b>Docket:</b>	MIPS.0170-00-US
<b>For:</b>	FETCH AND DISPATCH DISASSOCIATION APPARATUS FOR MULTI-STREAMING PROCESSORS

**INFORMATION DISCLOSURE STATEMENT  
SUBMITTED UNDER 37 CFR 1.97(D) AND 1.97 (E)(2)**

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement. The information cited in the information disclosure statement was cited by the United States Patent Office in one or more applications that are related to the present application.

Also attached hereto is a check in the amount of **\$180.00** for the fee set forth in § 1.17(p).

01/11/2005 BABRAHA1 00000101 09706154

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Respectfully submitted,

  
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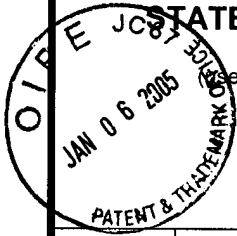
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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

Use as many sheets as necessary



## **Complete if Known**

Application Number	09/706154
Filing Date	11/03/00
First Named Inventor	Enrique Musoll Adolfo M. Nemirovsky Mario D. Nemirovsky Narendra Sankar
Group Art Unit	2183
Examiner Name	Shane Gerstl
Attorney Docket Number	MIPS.0170-00-US

Sheet 1 of 2

## **U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup>			
	AA	5699537		Sharangpani et al	12/16/1997	
	AB	5724565		Dubey et al	03/03/1998	
	AC	5745778		Robert A. Alfieri	04/28/1998	
	AD	6105053		Kimmel et al	08/15/2000	
	AE	6141746		Kawano et al	10/31/2000	
	AF	6219780		Mikko Herman Lipasti	04/17/2001	
	AG	6343348		Tremblay et al	01/29/2002	
	AH	6378063		Corwin et al	04/23/2002	
	AI	6542991		Joy et al	04/01/2003	
	AJ	6691221		Joshi et al	02/10/2004	

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number <sup>4</sup>	Kind Code <sup>5</sup>			

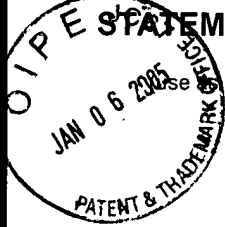
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<sup>2</sup>Unique citation designation number. <sup>3</sup>See attached Kinds of U.S. Patent Documents. <sup>4</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>5</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>6</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>7</sup>Applicant is to place a check mark here if English language Translation is attached.

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Use as many sheets as necessary				Application Number	09/706154
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				First Named Inventor	Enrique Musoll Adolfo M. Nemirovsky Mario D. Nemirovsky Narendra Sankar
				Group Art Unit	2183
				Examiner Name	Shane Gerstl
Sheet	2	of	2	Attorney Docket Number	MIPS.017-00-US

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
	BA	<i>The PowerPC Architecture: A Specification for a New Family of RISC Processors</i> , Second Edition, Morgan Kaufmann, San Francisco, pp.70-92 (May 1994)		
	BB	<i>MC68020 32-Bit Microprocessor User's Manual</i> , Third Edition, Prentice Hall, New Jersey, pp. 3-125, 3-126, and 3-127 (1989)		
	BC	Potel, M.J., "Real-Time Playback in Animation Systems," <i>Proceedings of the 4<sup>th</sup> Annual Conference on Computer Graphics and Interactive Techniques</i> , San Jose, CA, pp. 72-77 (1977)		
	BD	<i>ARM Architecture Reference Manual</i> , Prentice Hall, pp. 3-41, 3-42, 3-43, 3-67, 3-68 (1996)		
	BE	<i>ESA/390 Principles of Operation</i> , IBM Library Server, Table of Contents and Para.7.5.31 and 7.5.70 (1993) (available at: <a href="http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENTS">http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENTS</a> )		
	BF	<i>MC88110 Second Generation RISC Microprocessor User's Manual</i> , Motorola, Inc., pp.10-66, 10-67, and 10-71 (1991)		
	BG	Diefendorff, Keith et al., "Organization of the Motorola 88110 Superscalar RISC Microprocessor," <i>IEEE Micro</i> , Vol, 12, No. 2, pp.40-63 (1992)		
	BH	Kane, Gerry, <i>PA-RISC 2.0 Architecture</i> , Prentice Hall, New Jersey, pp. 7-106 and 7-107 (1996)		
	BI	Diefendorff, Keith et al, "AltiVec Extension to PowerPC Accelerates Media Processing," <i>IEEE Micro</i> , Vol, 20, No. 2, pp.85-95 (March-April 2000)		
	BJ	Diefendorff, Keith, "WinChip 4 Thumbs Nose at ILP," <i>Microprocessor Report</i> , Vol, 12, No.16, 10 pages (December 7, 1998)		
	BK	Gwennap, Linley, "Digital 21264 Sets New Standard," <i>Microprocessor Report</i> , Vol, 10, No. 14, 11 pages (October 28, 1999)		
	BL	Diefendorff, Keith, "Power4 Focuses on Memory Bandwidth," <i>Microprocessor Report</i> , Vol, 13, No. 13, 13 pages (October 6, 1999)		
	BM	Diefendorff, Keith, "K7 Challenges Intel," <i>Microprocessor Report</i> , Vol, 12, No. 14, 7 pages (October 26, 1998)		

Examiner Signature		Date Considered	
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